NSD914XV2T1

Preferred Device

High-Speed Switching Diode

Features

- High-Speed Switching Applications
- Lead Finish: 100% Matte Sn (Tin)
- Qualified Maximum Reflow Temperature: 260°C
- Extremely Small SOD-523 Package
- Pb-Free Package is Available

MAXIMUM RATINGS ($T_A = 25^{\circ}C$)

Rating	Symbol	Max	Unit
Reverse Voltage	V _R	100	V
Forward Current	١ _F	200	mAdc
Peak Forward Surge Current	I _{FM(surge)}	500	mAdc

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR–5 Board (Note 1) $T_A = 25^{\circ}C$ Derate above 25°C	P _D	200 1.57	mW mW/°C
Thermal Resistance Junction-to-Ambient	R_{\thetaJA}	635	°C/W
Junction and Storage Temperature	T _J , T _{stg}	150	°C

1. FR-4 @ Minimum Pad.

Diode Capacitance

Forward Voltage

 $(I_{\rm F} = 10 \, {\rm mAdc})$ **Reverse Recovery Time**

 $(I_{\rm F} = I_{\rm R} = 10 \, \rm{mAdc})$

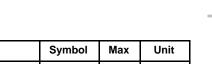
 $(V_R = 0 V, f = 1.0 MHz)$

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Reverse Breakdown Voltage (I _{BR} = 100 μAdc)	V _(BR)	100	_	Vdc
Reverse Voltage Leakage Current (V _R = 20 Vdc) (V _R = 75 Vdc)	I _R	_	25 5.0	nAdc μAdc

CD

VF

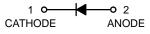
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MARKING DIAGRAM

SOD-523 **CASE 502** PLASTIC



5D = Specific Device Code

= Date Code Μ

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping†
NSD914XV2T1	SOD-523	3000/Tape & Reel
NSD914XV2T1G	SOD-523 (Pb-Free)	3000/Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

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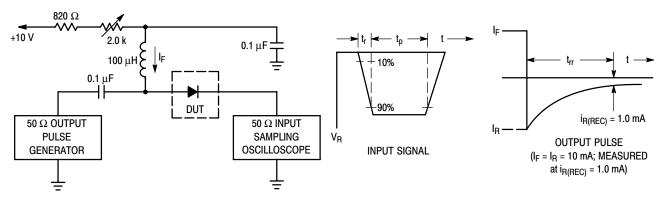
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pF

Vdc

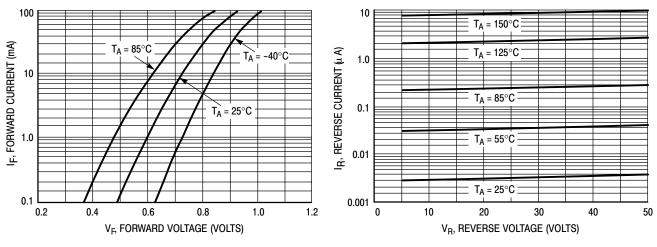
ns

NSD914XV2T1



Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 10 mA. 2. Input pulse is adjusted so I_{R(peak)} is equal to 10 mA. 3. t_p » t_{rr}

Figure 1. Recovery Time Equivalent Test Circuit



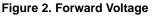


Figure 3. Leakage Current

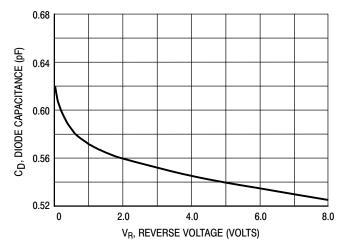
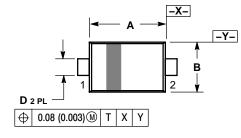


Figure 4. Capacitance

PACKAGE DIMENSIONS

SOD-523 CASE 502-01 ISSUE B



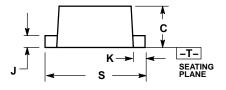


1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982

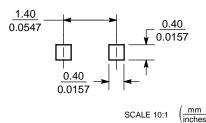
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CONTROLLING DIMENSION: MILLIMETER. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL. 3

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	1.10	1.20	1.30	0.043	0.047	0.051	
в	0.70	0.80	0.90	0.028	0.032	0.035	
С	0.50	0.60	0.70	0.020	0.024	0.028	
D	0.25	0.30	0.35	0.010	0.012	0.014	
ſ	0.07	0.14	0.20	0.0028	0.0055	0.0079	
ĸ	0.15	0.20	0.25	0.006	0.008	0.010	
S	1.50	1.60	1.70	0.059	0.063	0.067	



SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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